

504-925

PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10053818	01/22/2002	324	754	229	NGUYEN, VINH

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\*\*CONTINUING DATA VERIFIED:

\*\* FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	ATTORNEY DOCKET NO
Foreign priority claimed: <input type="checkbox"/> yes <input type="checkbox"/> no			S1
35 USC 119 conditions met: <input type="checkbox"/> yes <input type="checkbox"/> no			
Verified and Acknowledged Examiners's initials			
TITLE : Testing integrated circuits			

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
ISSUE FEE Amount Due      Date Paid		Assistant Examiner	Total Claims      Print Claim for O.G.
		Primary Examiner	DRAWING Sheets Drwg.      Figs. Drwg.      Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner
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